

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Patent Application of: Yoshito Katano

Application	10/597,353	Group Art Unit:	2113
Filing date:	July 6, 2007	Confirmation No.	9812
Customer No.:	26263	Examiner:	Yolanda L. Wilson
For:	<b>SEMICONDUCTOR DEVICE AND PROCESSING METHOD FOR STARTING THE SAME</b>		

**Electronically filed via EFS-Web**

Commissioner for Patents  
P.O. Box 1450  
Arlington, VA 22313-1450

**INFORMATION DISCLOSURE STATEMENT**

In accordance with the provisions of 37 C.F.R. § 1.56 and 37 C.F.R. §1.97(b)(4), Applicants request that citation and examination of the references identified on the attached PTO/SB/08a form, copies of which are enclosed herewith in accordance with 37 C.F.R. §1.98, be made during the course of examination of the above-referenced application for United States Letters Patent.

The references were cited in a Japanese Office Action dated October 27, 2009, issued in connection with counterpart Japanese Patent Application No. 2004-019278.

The filing of this information disclosure statement shall not be construed as a representation that a search has been made (37 C.F.R. § 1.97 (g)), an admission that the

information cited is, or is considered to be, material to patentability, or that no other material information exists.

The present Information Disclosure Statement is being submitted before the mailing of a first Office action after the filing of a request for examination under 37 C.F.R. §1.114.

Applicants believe that no fee is due. While Applicants believe that no fee is due, the Commissioner is hereby authorized to charge our Deposit Account No. 19-3140 for any fees required in connection with the filing of this Information Disclosure Statement.

Respectfully submitted,

Dated: December 8, 2009

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